


# Bitland Confidential

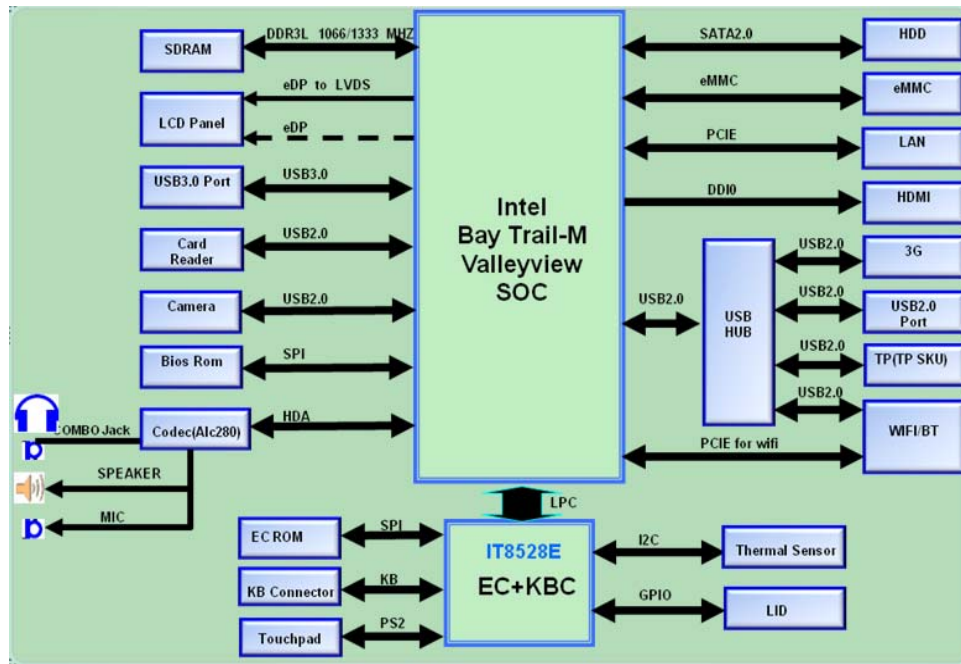
## Beetle M/B Schematics Document

Intel Baytrail-M Platform

2013-xx-xx

		Bitland Information Technology Co., Ltd.	
Page Name			
Size A3	Project Name <Doc>	Sheet 1 of 42	Rev Rev1.0
Date: Tuesday, March 04, 2014			
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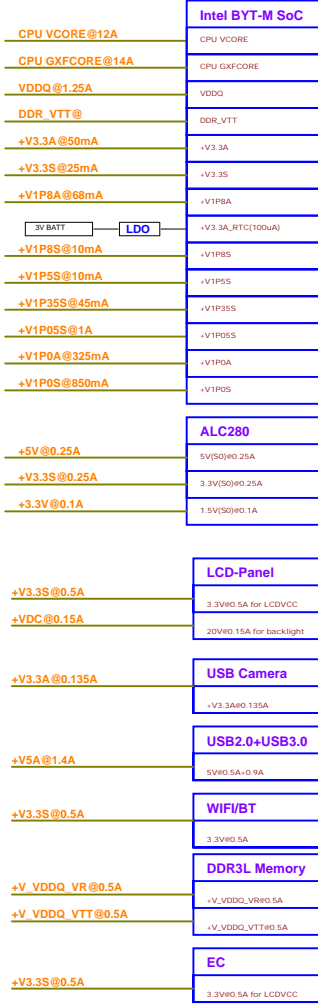
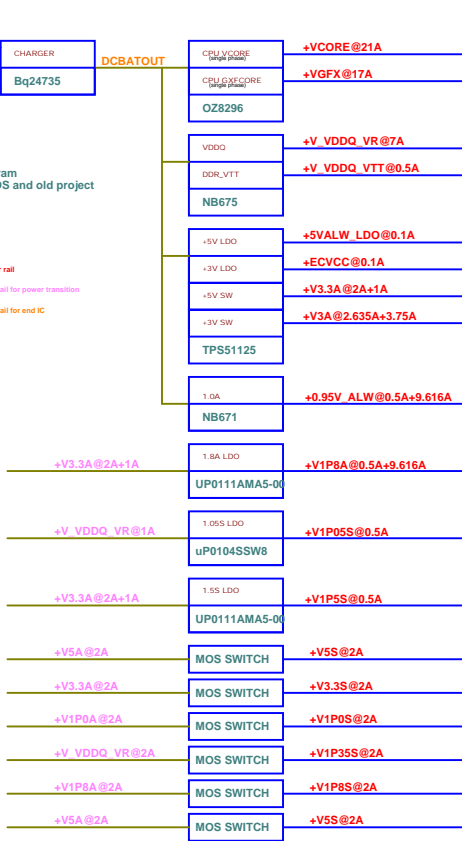


Notes:  
1.remove 1.2A in diagram  
2.current data from EDS and old project



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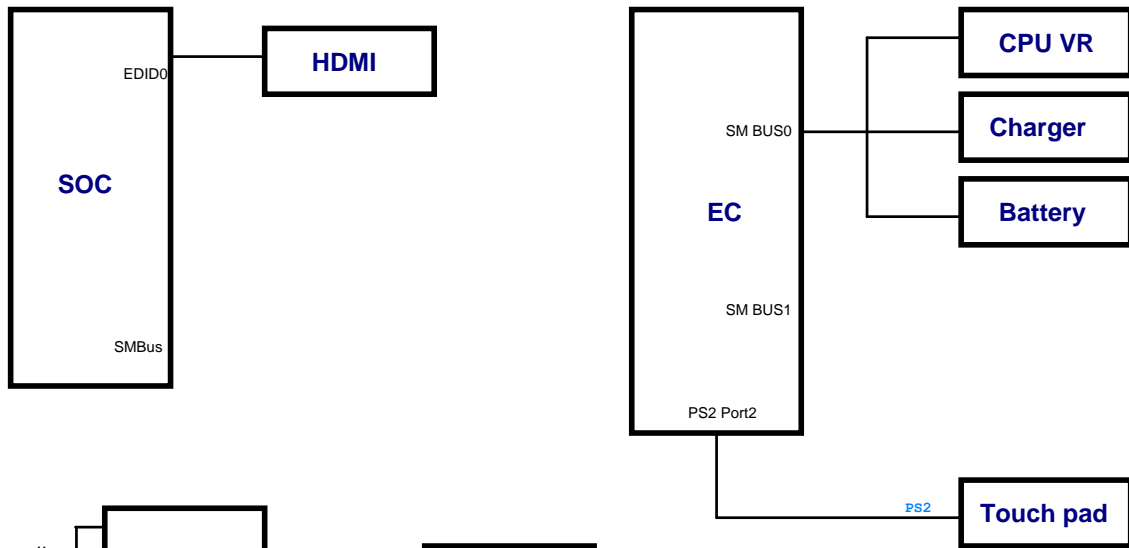
#### Power States

Power Rail	On S0	On S3	On S4/S5	On G3
VCORE	ON	OFF	OFF	OFF
VGFX	ON	OFF	OFF	OFF
V1P0S	ON	OFF	OFF	OFF
V1P0S5	ON	OFF	OFF	OFF
V1P6S	ON	OFF	OFF	OFF
V1P6S5	ON	OFF	OFF	OFF
V1P6S	ON	OFF	OFF	OFF
V3P3S	ON	OFF	OFF	OFF
V5S	ON	OFF	OFF	OFF
VDDQ_VTT	ON	OFF	OFF	OFF
VDDQ_VR	ON	ON	OFF	OFF
V1P0A	ON	ON	ON	OFF
V1P2A	ON	ON	ON	OFF
V1P8A	ON	ON	ON	OFF
V3P3A	ON	ON	ON	OFF
V5A	ON	ON	ON	OFF

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SMBus block diagram




Clock block diagram

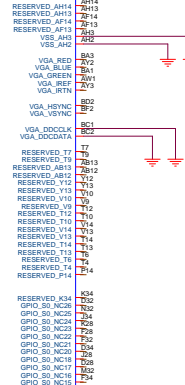
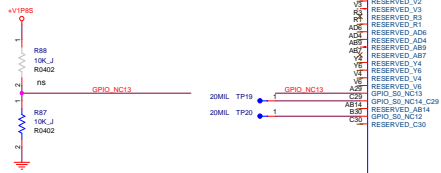
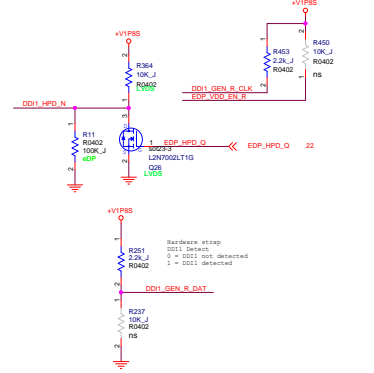
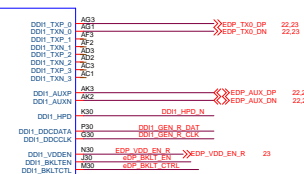
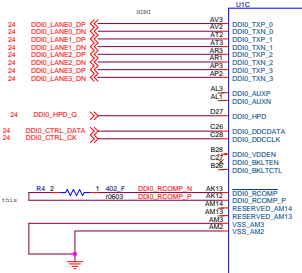
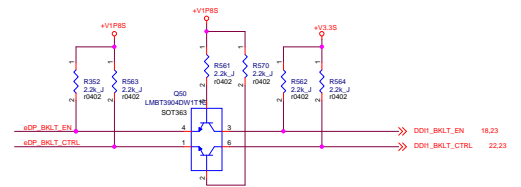
<b>BIT</b>		<b>Bitland Information Technology Co., Ltd.</b>	
Page Name			
Size A3	Project Name <Doc>		Rev Rev1.0
Date: Tuesday, March 04, 2014	Sheet 4 of 42		
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		<b>Bitland Information Technology Co.,Ltd.</b>	
Page Name			
Size A4	Project Name <Doc>		Rev Rev1.0
Date:	Tuesday, March 04, 2014	Sheet	5 of 42
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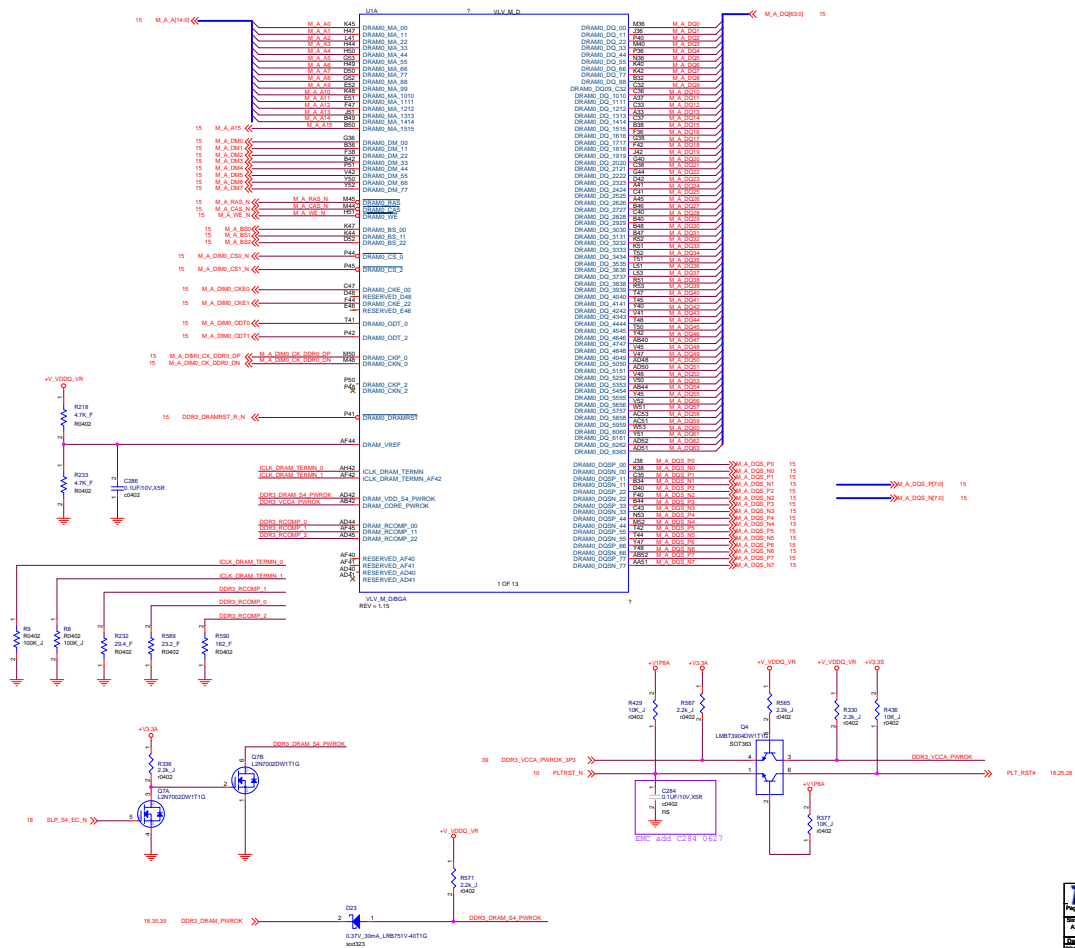
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 <b>Bitland Information Technology Co., Ltd.</b>	
Page Name	
Size C	Project Name <Doc>
	Rev Rev1.0
Date: Tuesday, March 04, 2014	Sheet 6 of 42

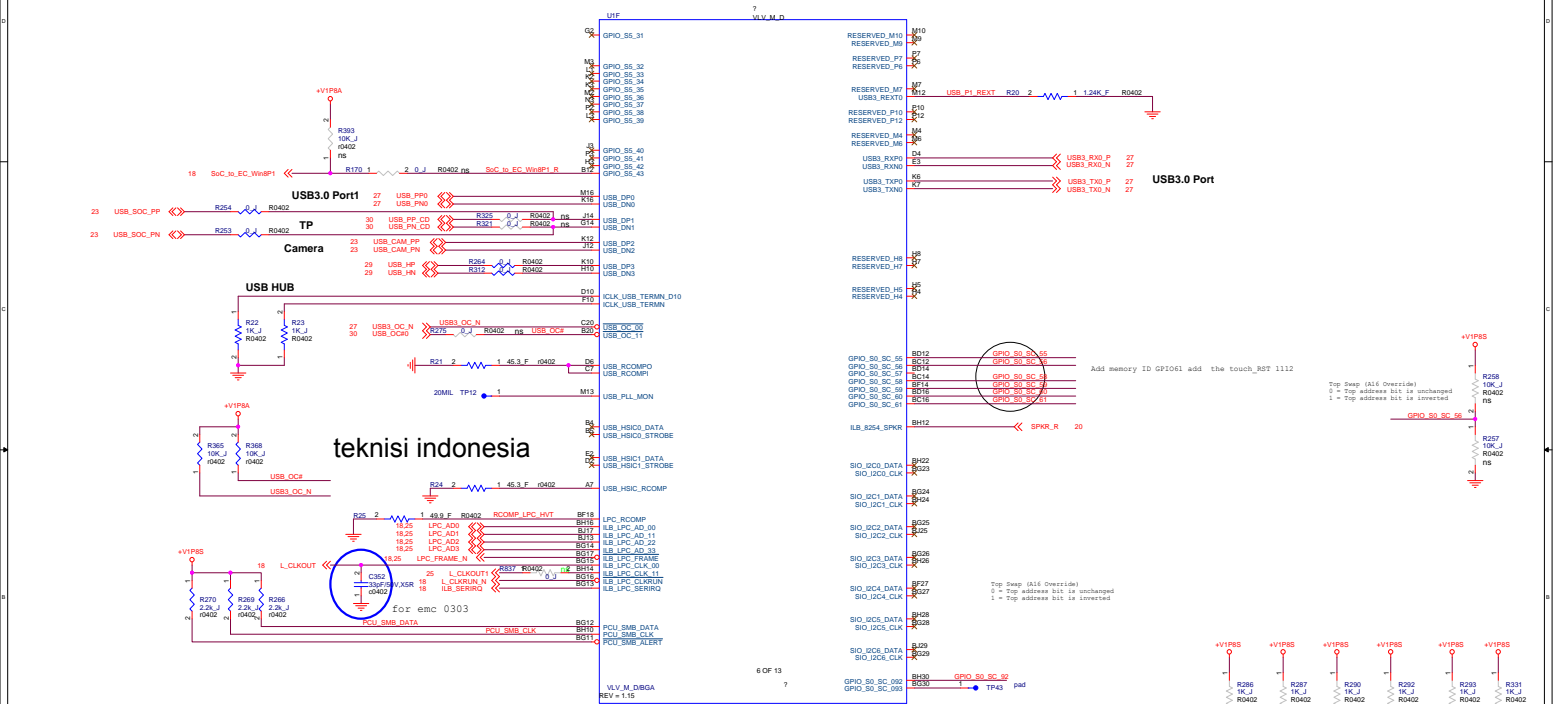
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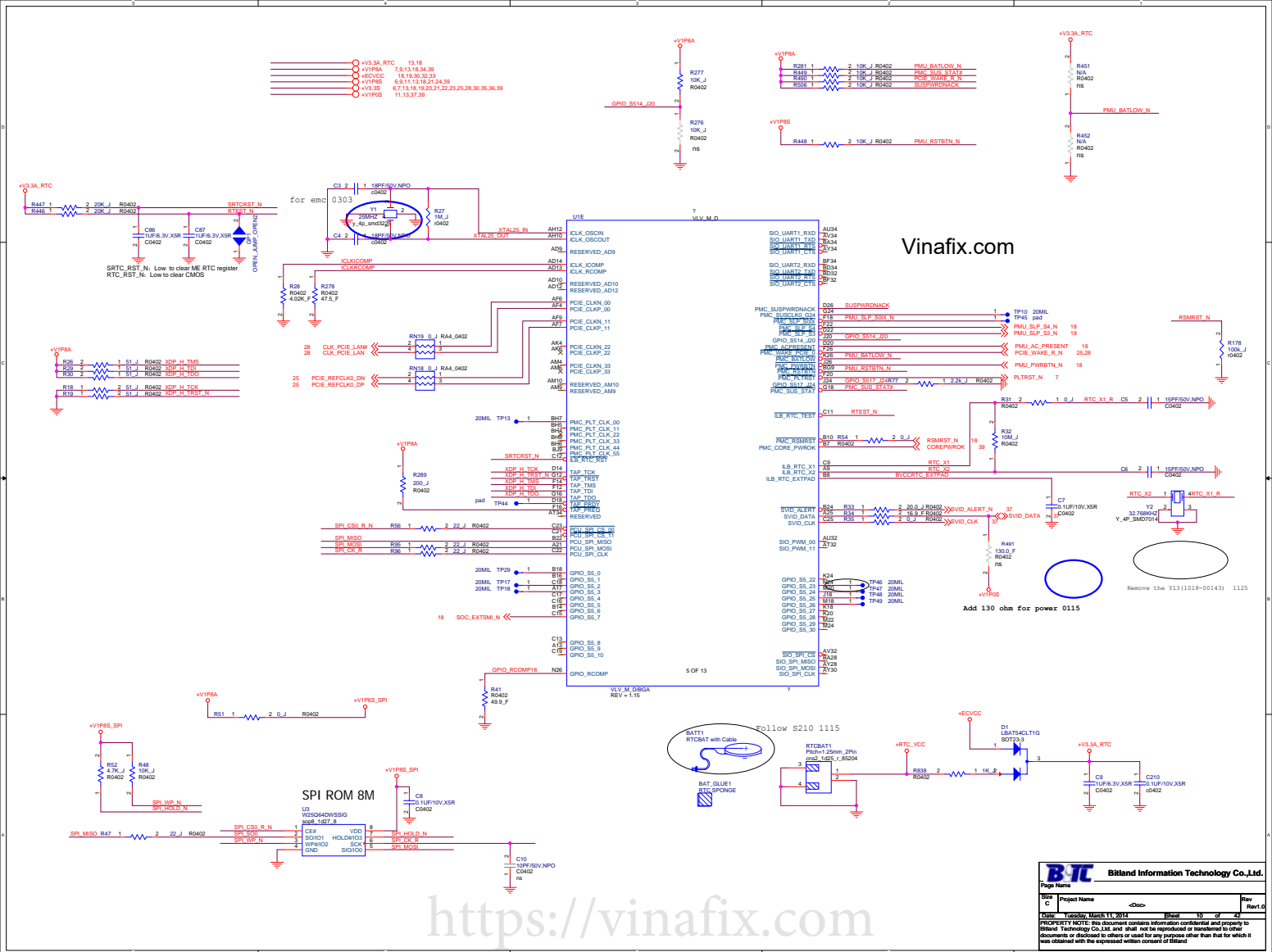




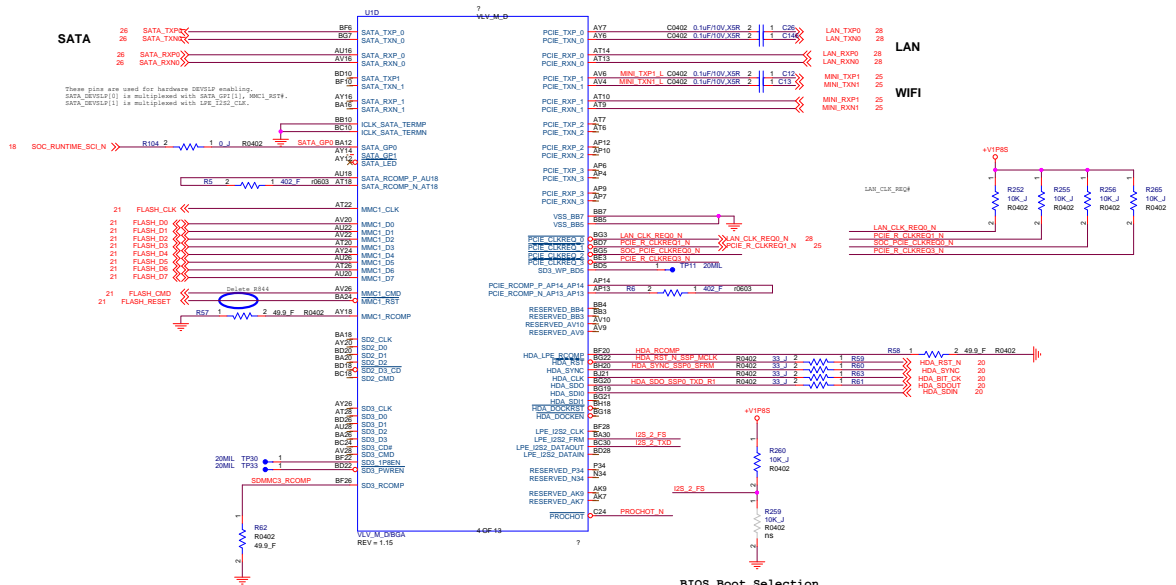


 <b>Bitland Information Technology Co., Ltd.</b>		
Page Name		
Size C	Project Name  <Doc>	Rev Rev1.0
Date: Wednesday, April 02, 2014		Sheet 9 of 42

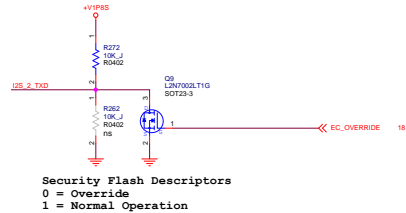
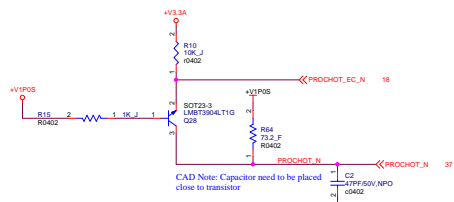
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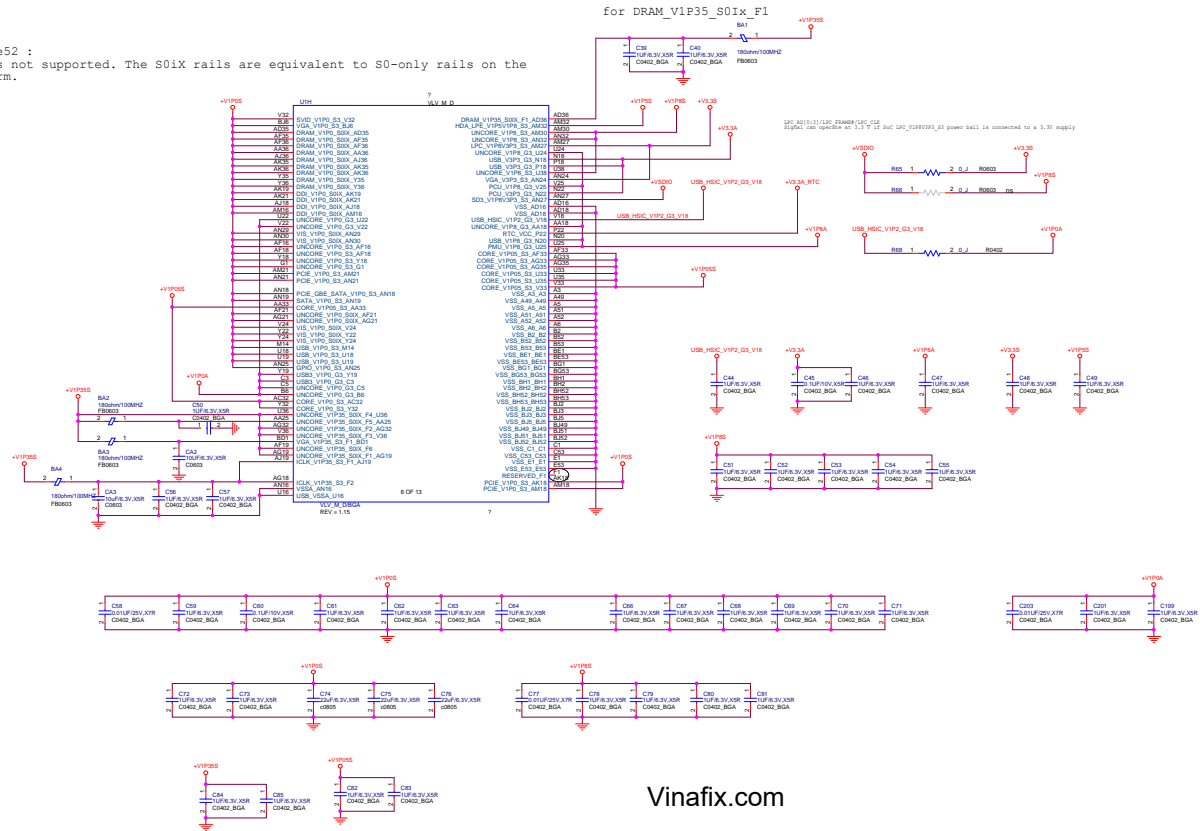
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BIT Bitland Information Technology Co.,Ltd.			
Page Name			
Rev	Project Name	<Doc>	Rev
C	Yashraj March 2014	Sheet	11 of 42
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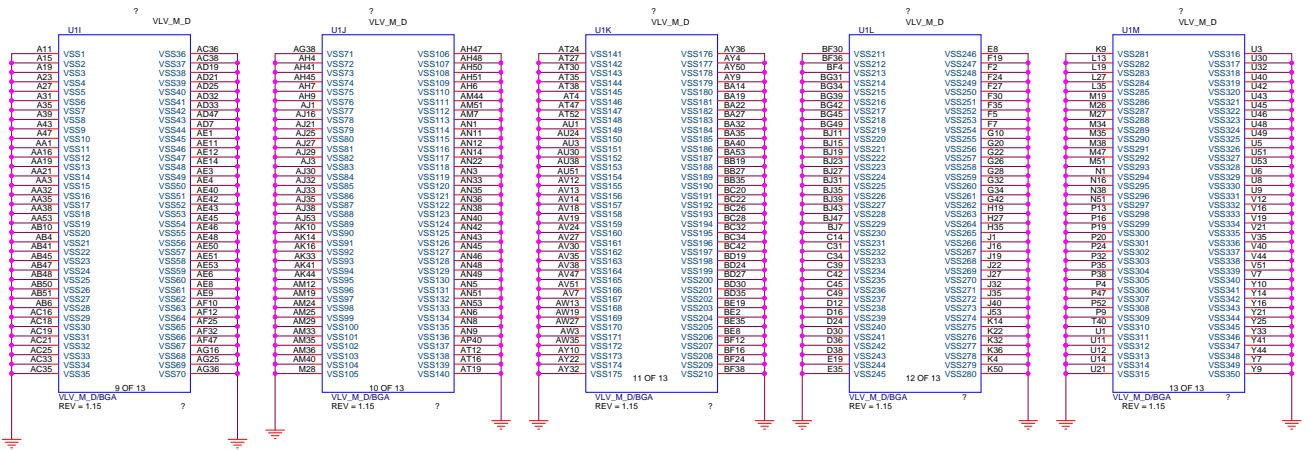


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<b>B.I.T.</b> Bitland Information Technology Co., Ltd.	
Page Name	Rev
1	1
Project Name	Rev
1	1
2	1
3	1
4	1
5	1
6	1
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10	1
11	1
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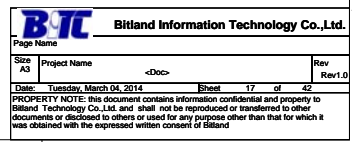
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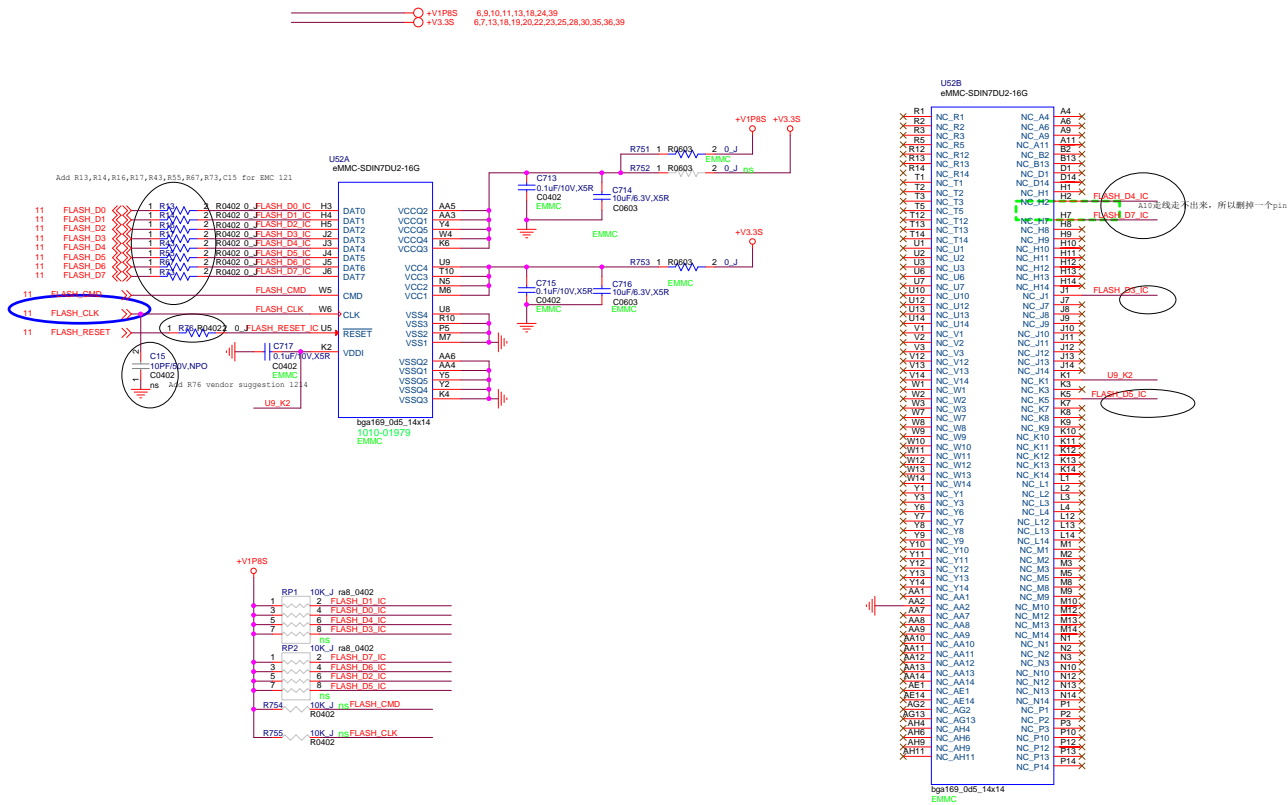










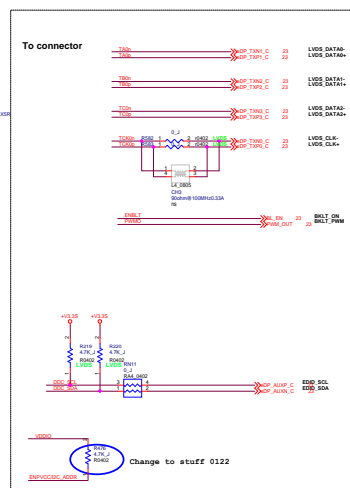
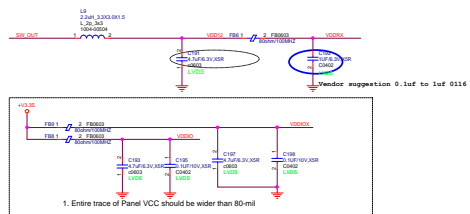
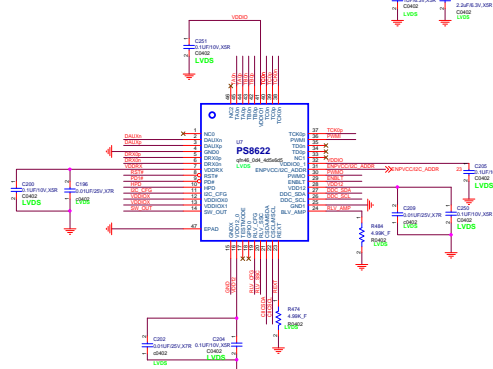
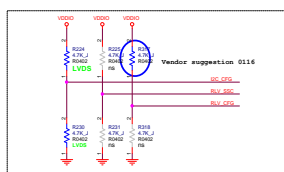


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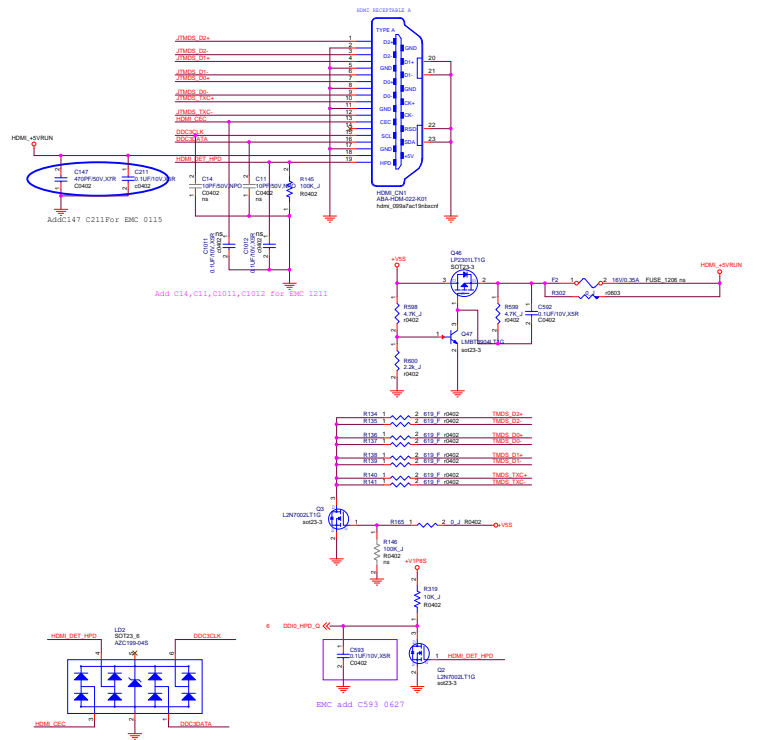
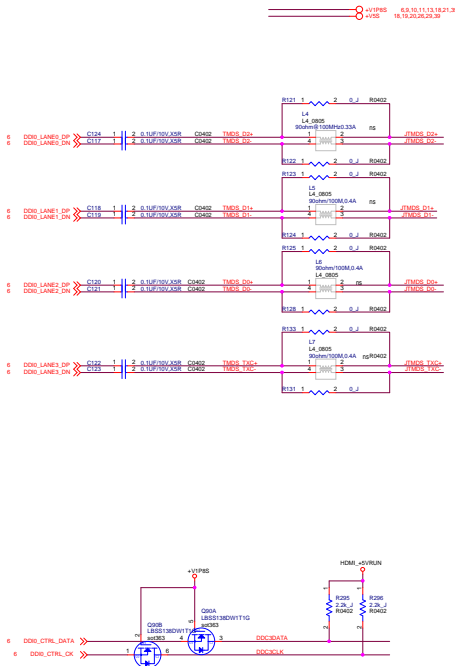
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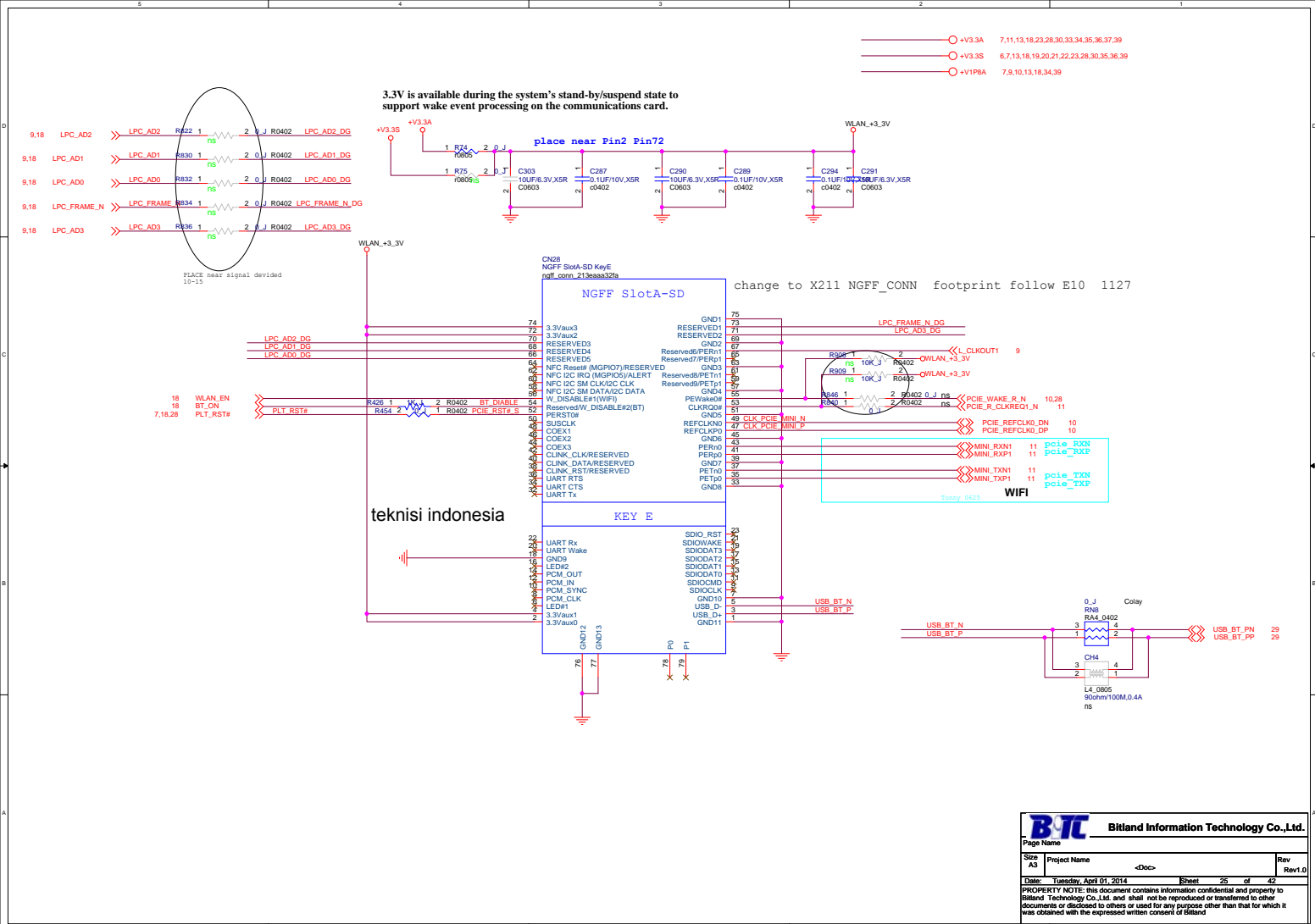
BTL Bitland Information Technology Co., Ltd.			
Page Name			
Size	Project Name	Rev	Rev1.0
A3	<Doc>		
Date: Tuesday, March 04, 2014 Sheet 21 of 42			
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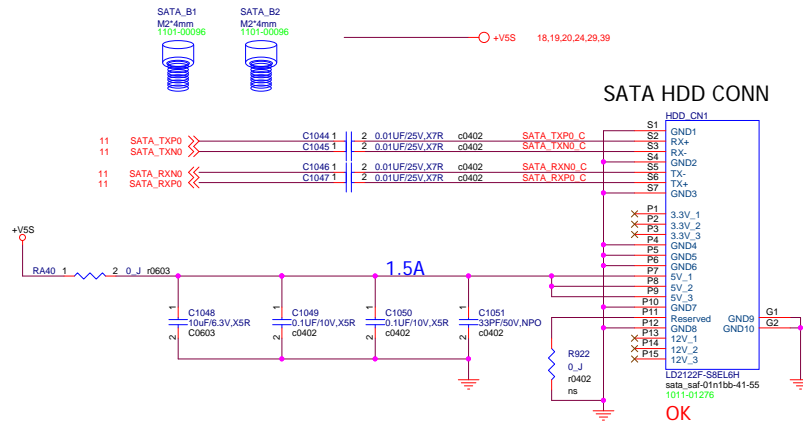








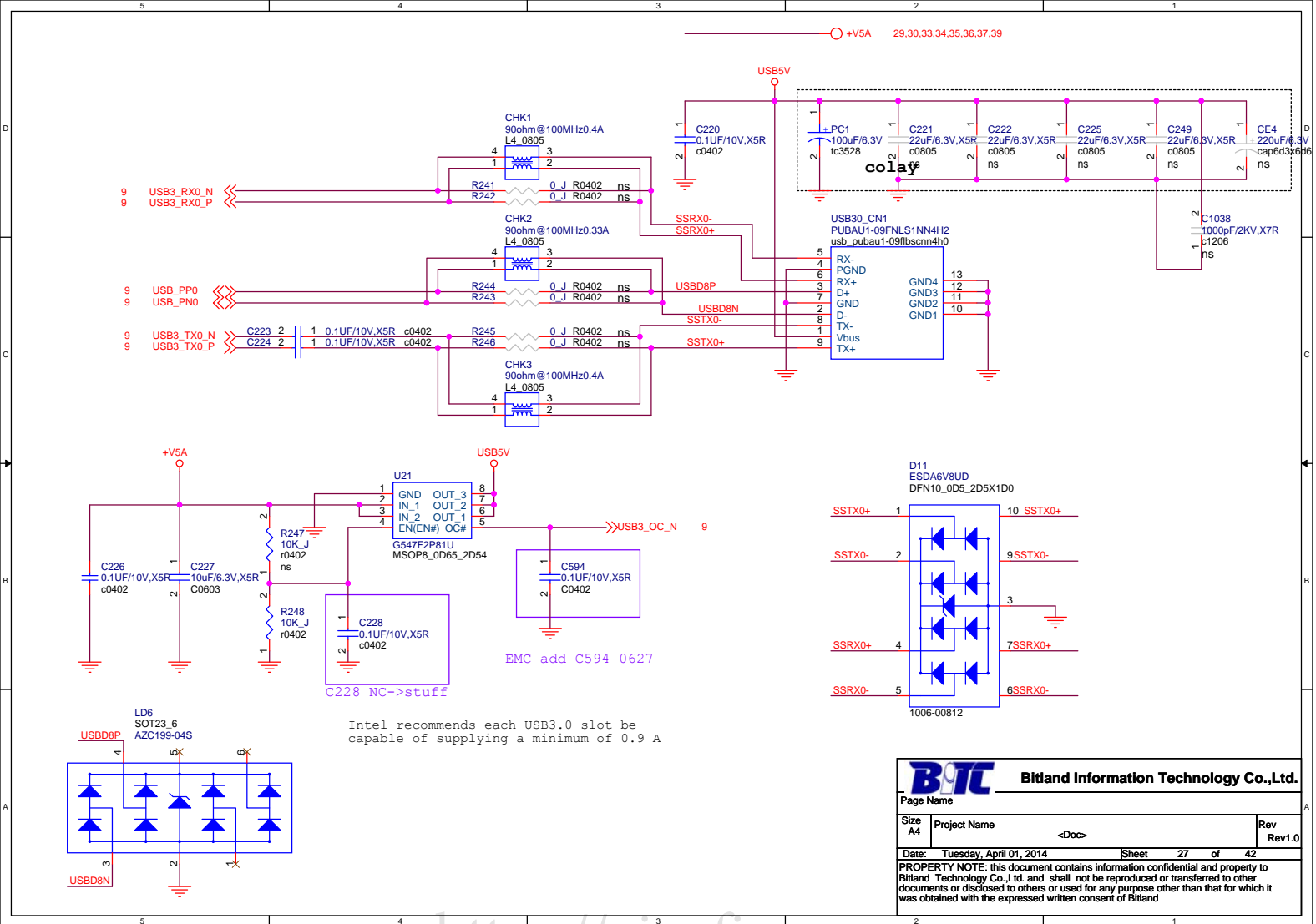


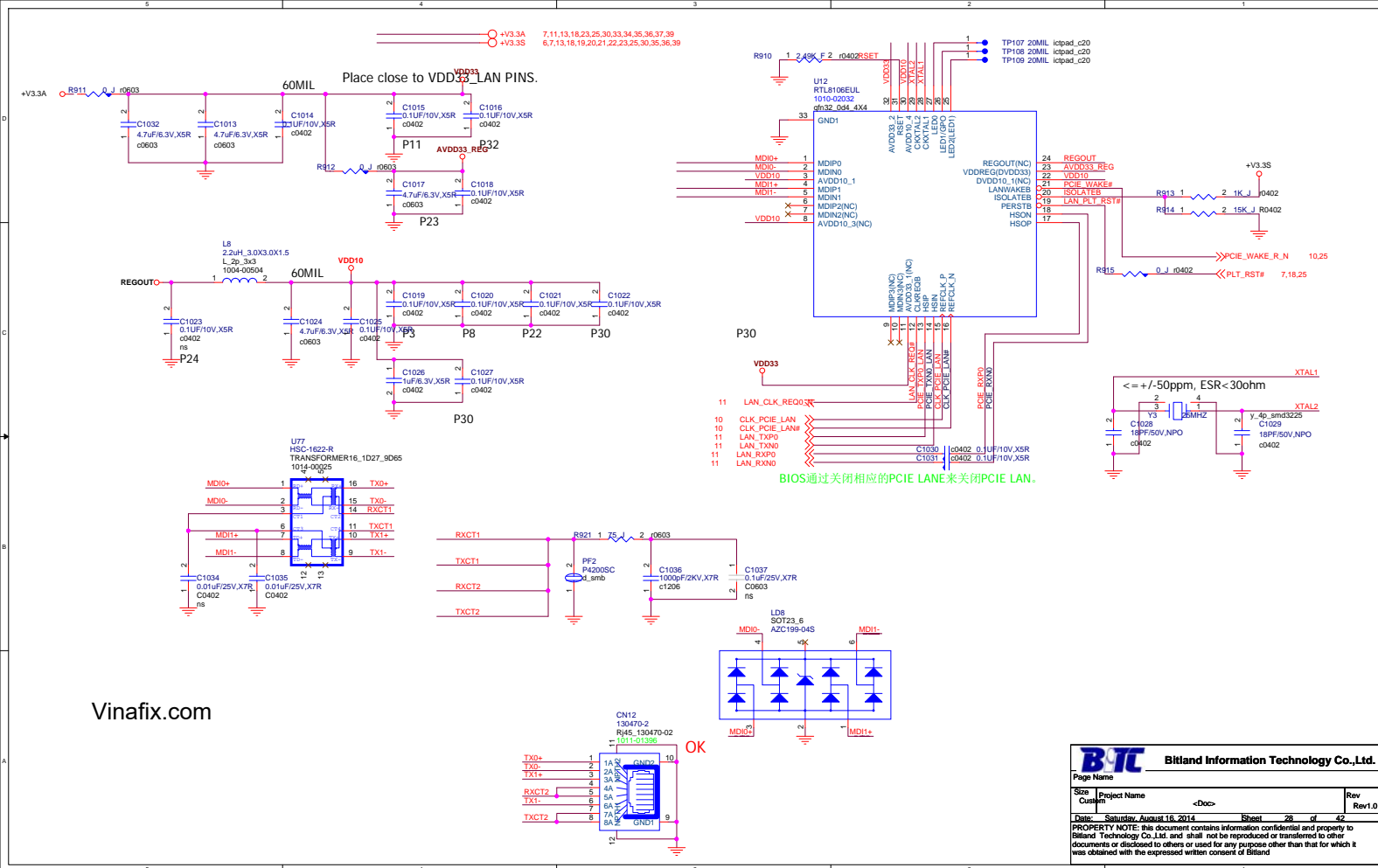


B-IT Bitland Information Technology Co.,Ltd.			
Page Name			
Size B	Project Name	<Doc>	Rev Rev1.0
Date:	Thursday, April 10, 2014	Sheet	26 of 42
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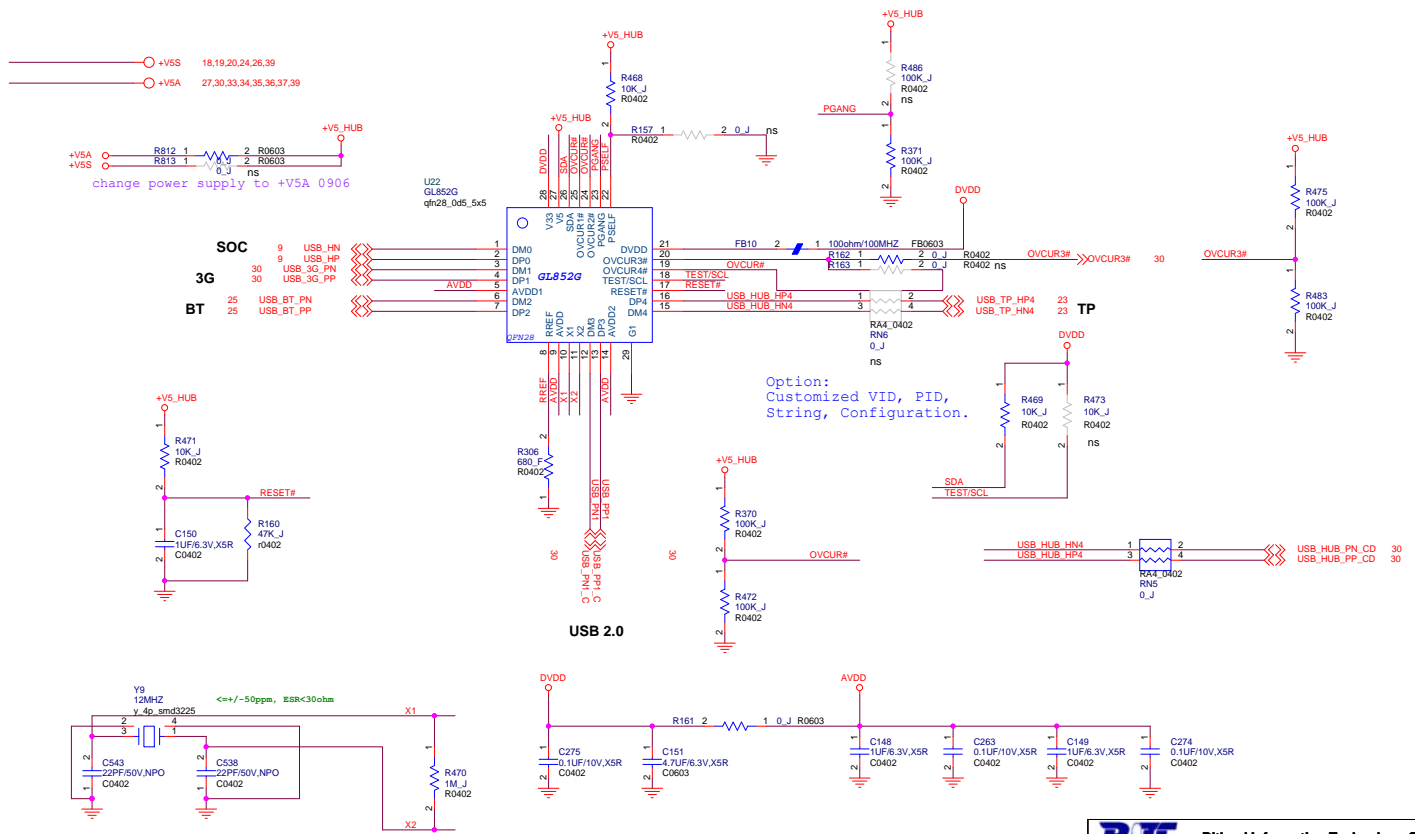
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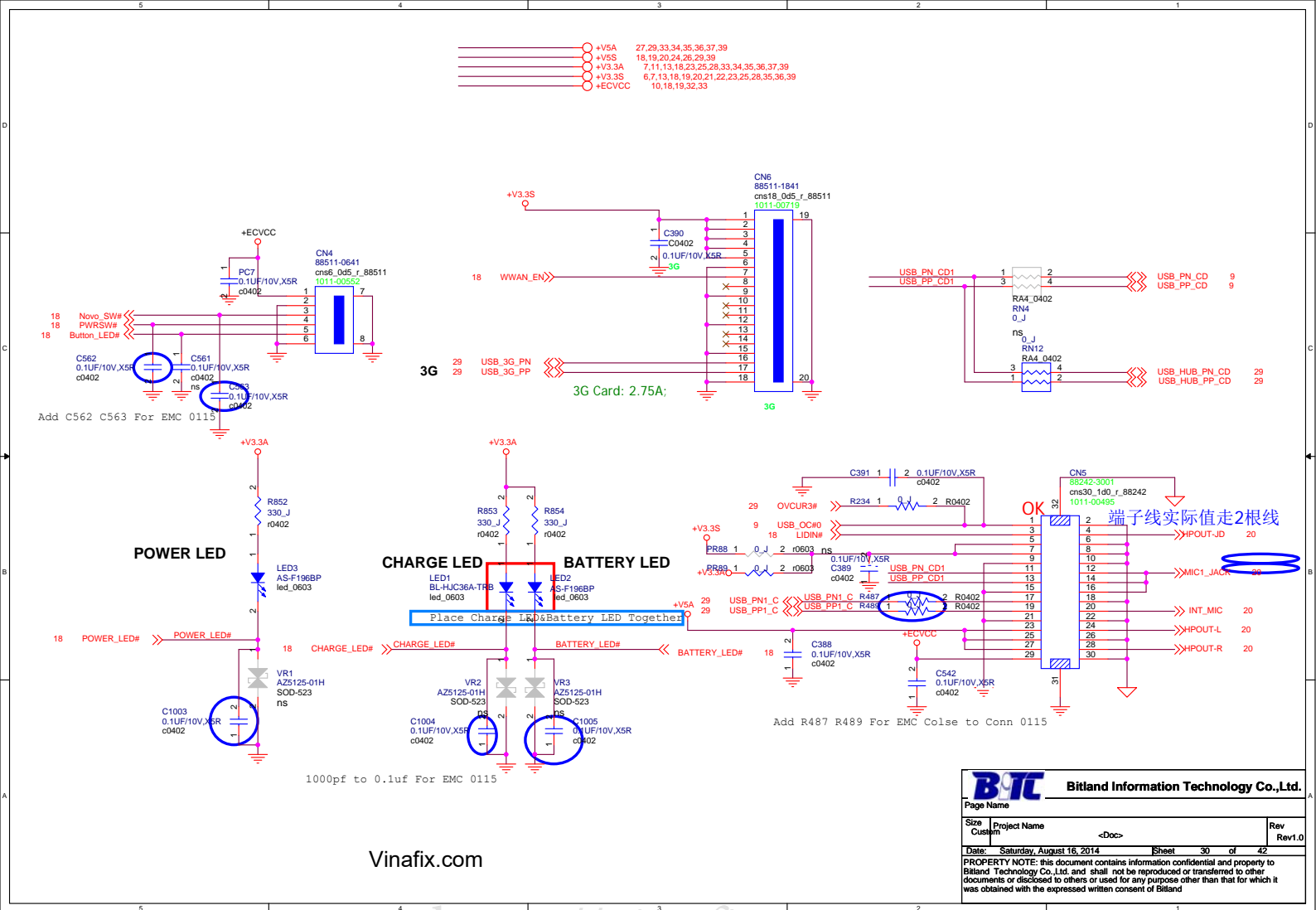


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BIT Bitland Information Technology Co.,Ltd.			
Page Name			
Size B	Project Name	<Doc>	Rev Rev1.0
Date: Wednesday, April 02, 2014	Sheet	29	of 42
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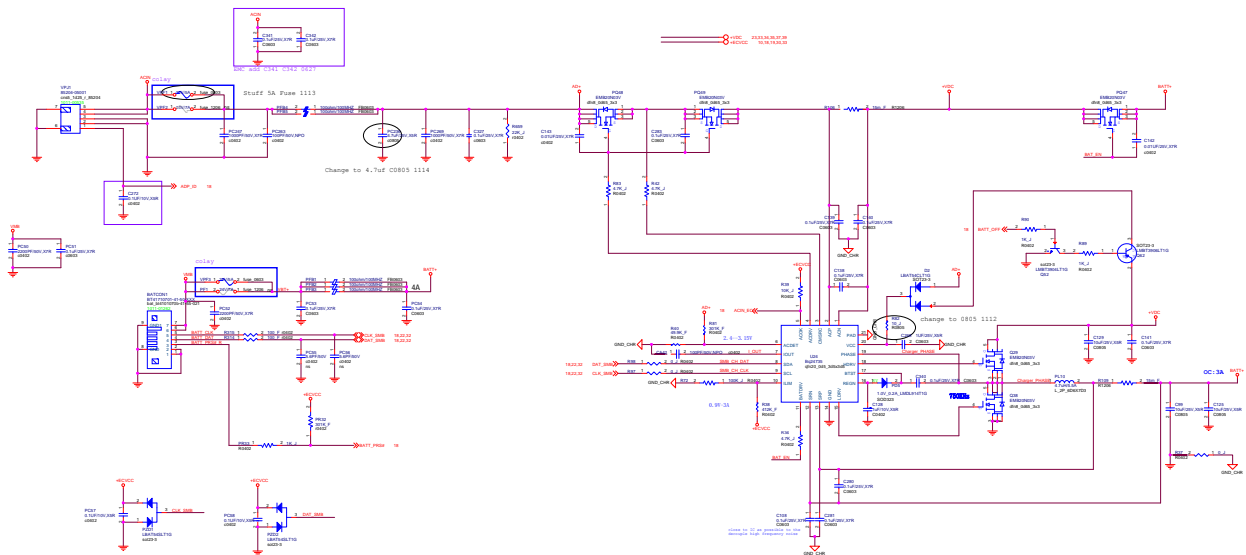
Page Name

Size A	Project Name  <Doc>	Rev Rev1.0
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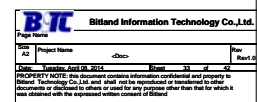
Date: Tuesday, March 04, 2014	Sheet 31 of 42
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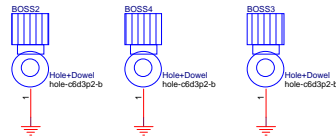
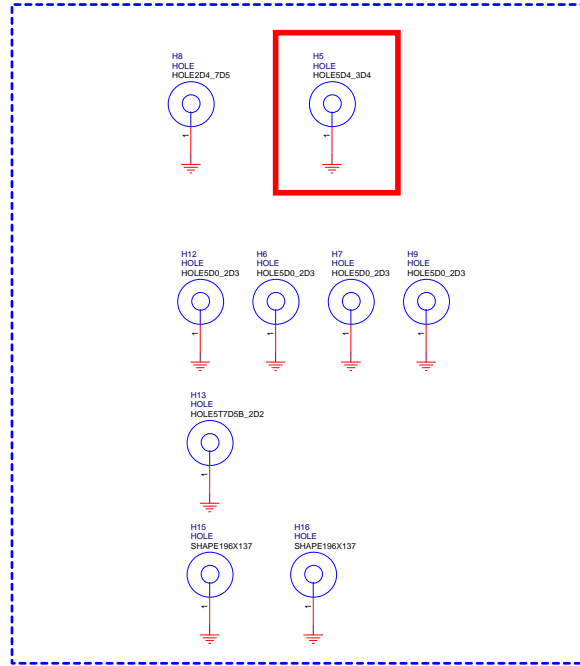
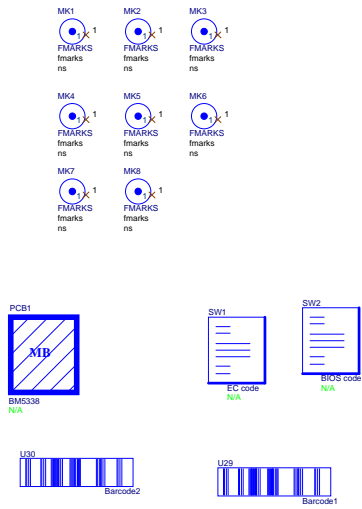









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 <b>Bitland Information Technology Co.,Ltd.</b>			
Page Name			
Size A3	Project Name  <Doc>	Rev Rev1.0	
Date: Tuesday, March 04, 2014		Sheet 38 of 42	
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**Bitland Information Technology Co.,Ltd.**

Page Name

Size A	Project Name  <Doc>	Rev Rev1.0
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Date:	Tuesday, March 04, 2014	Sheet	40	of	42
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Page Name																																																																																																																												
Size A		Project Name <Doc>																		Rev Rev1.0																																																																																																								
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